

Specification MAP/CV174/Issue 4 Dated 30.4.48... To be read in conjunction with K1001, ignoring clauses 5.2, 5.3, 5.8, 7.2.	<u>SECURITY</u>	
	<u>Specification</u> RESTRICTED	<u>Valve</u> UNCLASSIFIED

→ Indicates a change

<u>TYPE OF VALVE</u> - Tetrode <u>CATHODE</u> - Indirectly heated <u>ENVELOPE</u> - Glass-unmetallised		<u>MARKING</u> See K1001/4										
<u>RATING</u>		<u>BASE</u> T4 See K1001/AIV/D7										
		Note										
Heater Voltage (V)	12.6											
Heater Current (A)	1.75											
Max. Peak Anode Voltage (kV)	19.0											
Max. Screen Voltage (kV)	1.4											
Max. Anode Dissipation (W)	15.0											
Max. Screen Dissipation (W)	2.5											
Max. Grid Bias for out-off (V)	-800											
Max. Peak Anode Current (A)	5.0											
Max. Peak Screen Current (A)	0.6											
<u>CAPACITANCES (pF)</u>				<u>TOP CAP</u> See K1001/AI/D5.1								
Gae	11.0	<u>DIMENSIONS</u> See K1001/AI/D1.										
Gge	21.0	<table border="1"> <thead> <tr> <th>Dimension</th> <th>Min.</th> <th>Max.</th> </tr> </thead> <tbody> <tr> <td>Z (mm)</td> <td>152</td> <td>165</td> </tr> <tr> <td>B (mm)</td> <td>-</td> <td>57</td> </tr> </tbody> </table>		Dimension	Min.	Max.	Z (mm)	152	165	B (mm)	-	57
Dimension	Min.	Max.										
Z (mm)	152	165										
B (mm)	-	57										
Gag	0.5											
<u>NOTE</u>												
A. The above ratings apply for use as a series modulator with an off-on ratio of 400 : 1.												

To be performed in addition to those applicable in K1001.

	Test Conditions						Test	Limits		No. Tested	
	Vh	Vg1	Vg2	Va	Ig2	Ia		Min.	Max.		
a	12.6	0	0	0	0	0	Ih (A)	1.55	1.95	100%	
b	12.6	-	Strapped 200V. applied Maintained for 30 secs.		Ia + Ig2 = 75 mA		Final value of Reverse Ig (μA)	-	20.0	100%	
c	12.6	-	Strapped 200V. applied		Ia + Ig2 = 75 mA		Vg1 (V)	-30	-45	100%	
d	12.6	-	See Note 1		- 10μA		Vg1 (V)	-	-25	100%	
e	5.25	0	Strapped 300V. applied		-	-	Ic (mA)	15	-	100%	
f	See K1001/AIII						<u>CAPACITANCES</u> (pF)				
							Cae	6.9	11.1	1%	
							Cge	15.75	26.25	(1)	

NOTES

1. For test clause 'd' the anode and screen grid voltages shall be derived from the network as in Fig.1 on page 3.
2. The valve shall be processed during manufacture so that it will withstand an anode voltage of 19kV. peak without repeated flash-over.

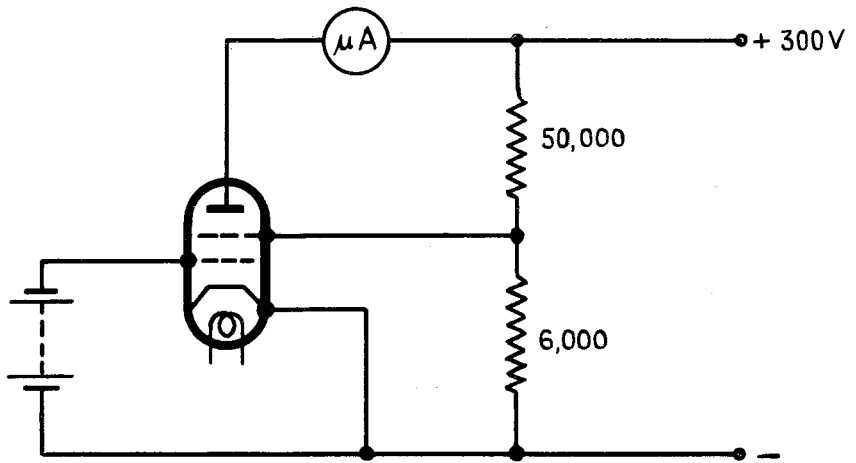


FIG. I.